



1/6

FIG. 1A

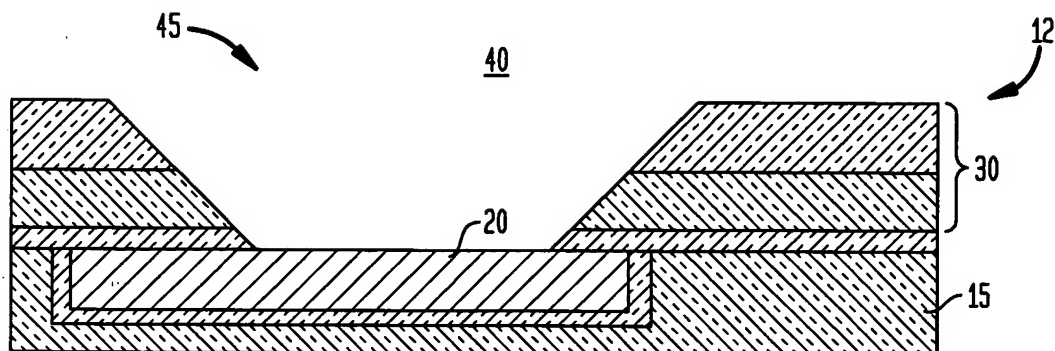
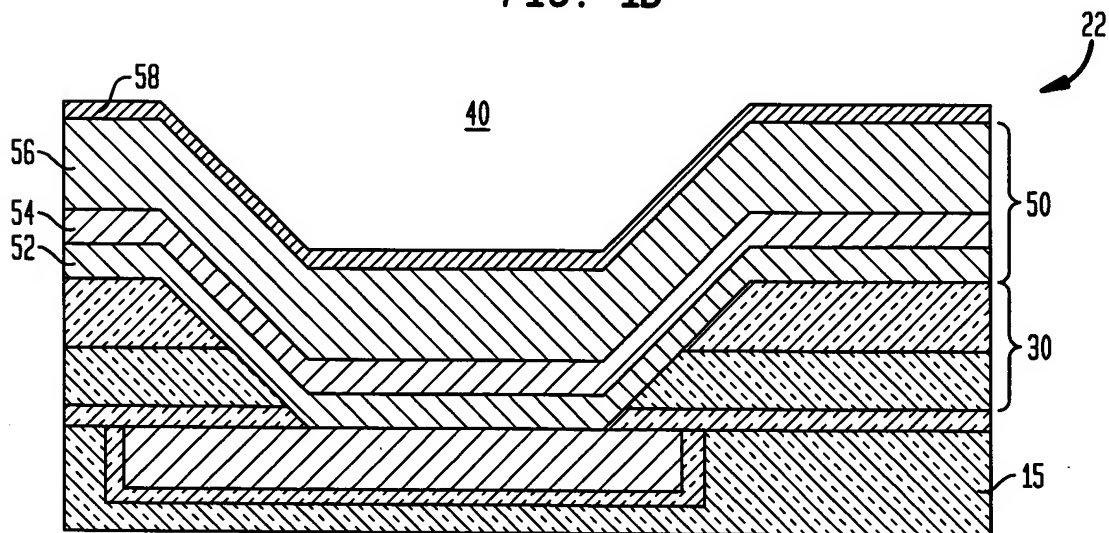


FIG. 1B



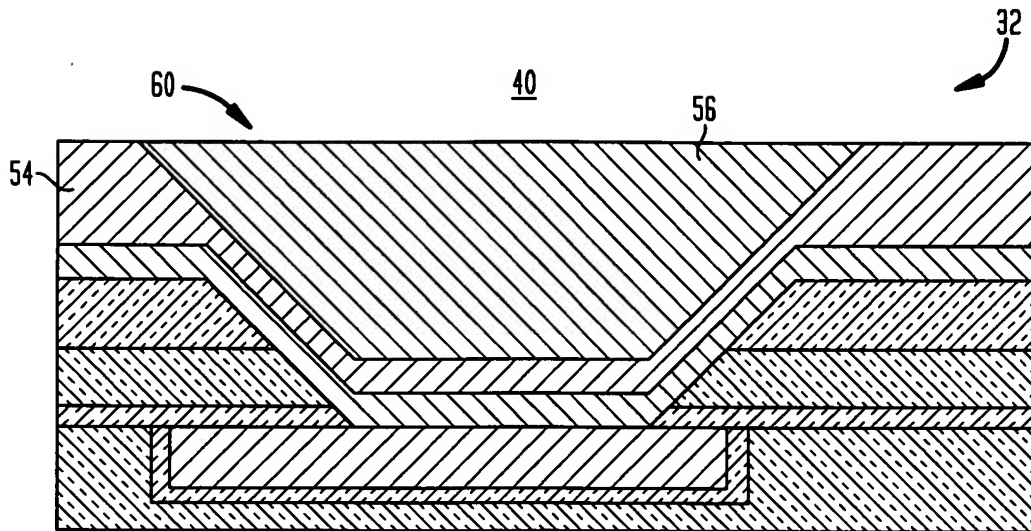
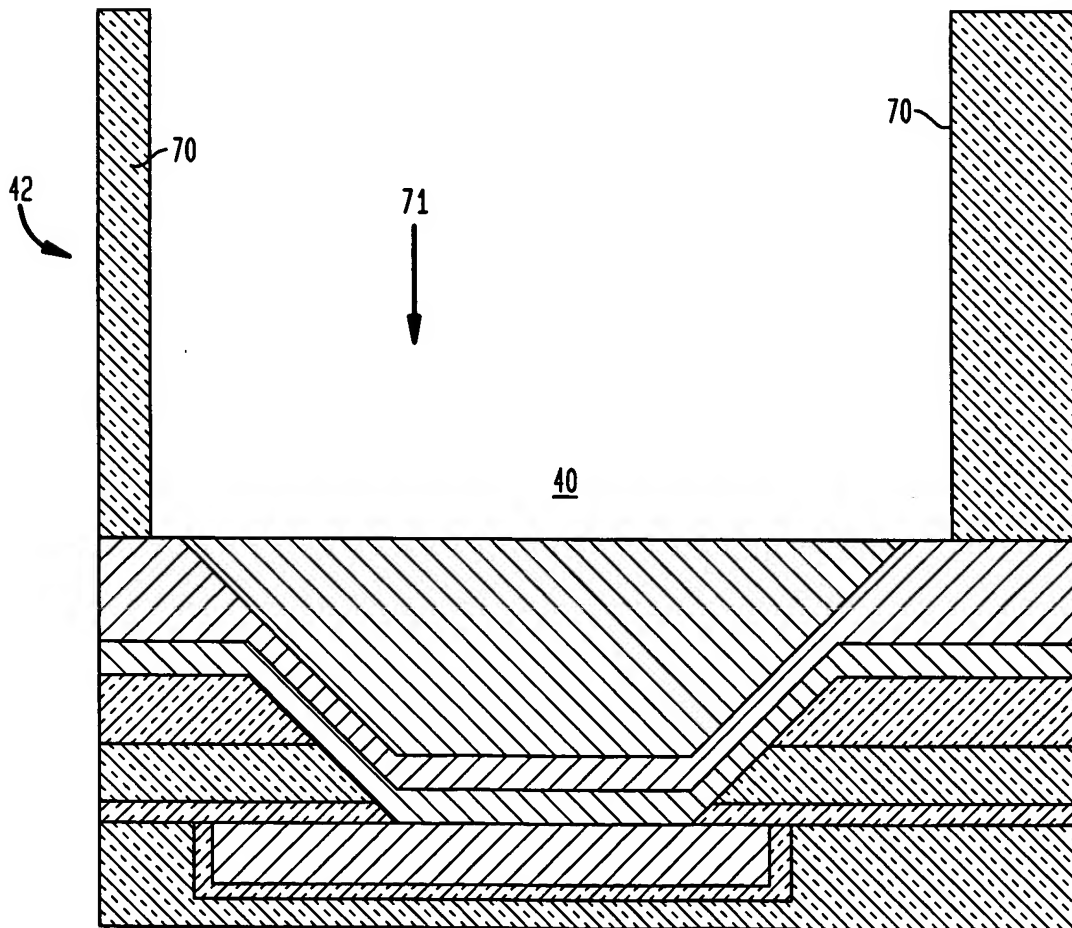
2/6
FIG. 1C**FIG. 1D**

Figure 1 is a cross-sectional view of a semiconductor device. The device includes a substrate 40, a gate stack 62, and a gate electrode 75. A gate electrode 52 is also shown, and a gate electrode 54 is indicated by a dashed line. A gate electrode 80 is shown on the left side of the gate stack 62.

[illegible]

BUR92004004BUS1

4/6

FIG. 2

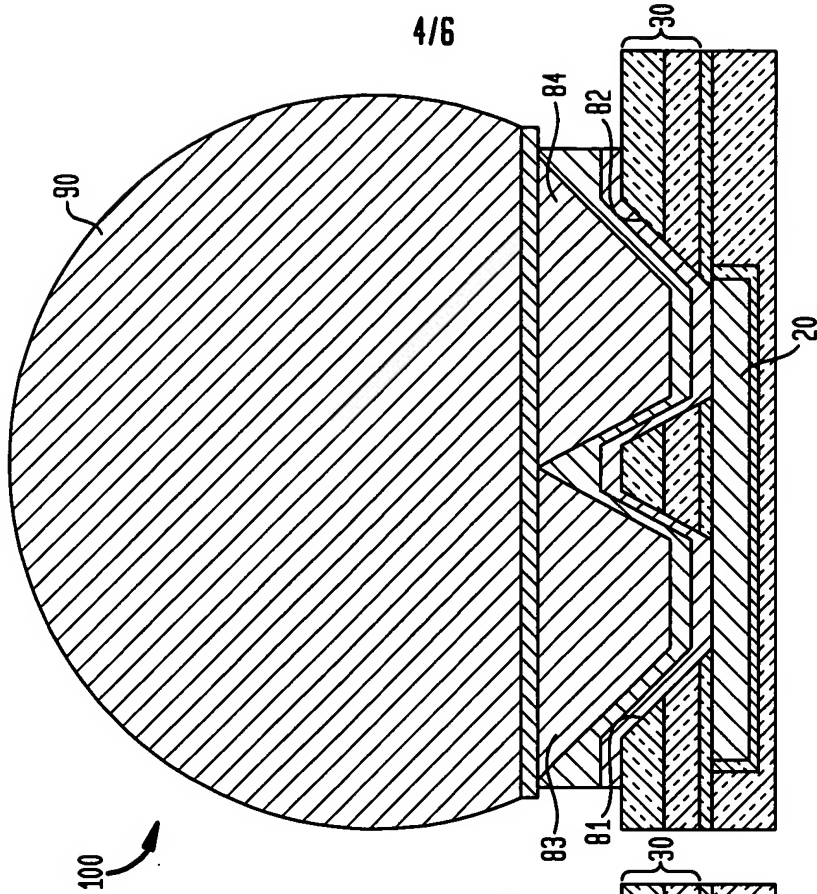


FIG. 1G

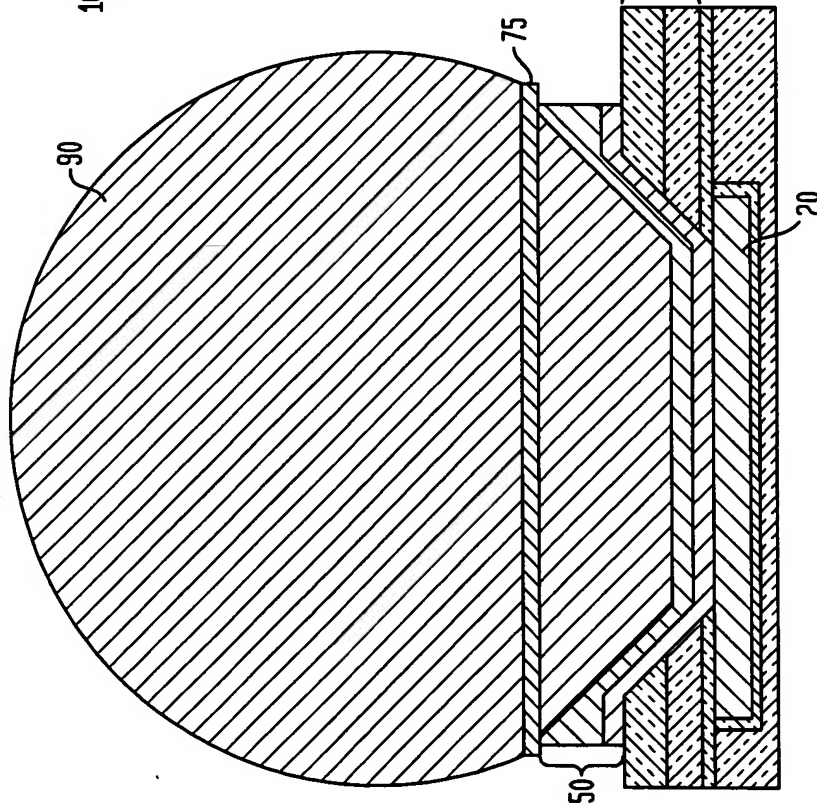


FIG. 3A

200

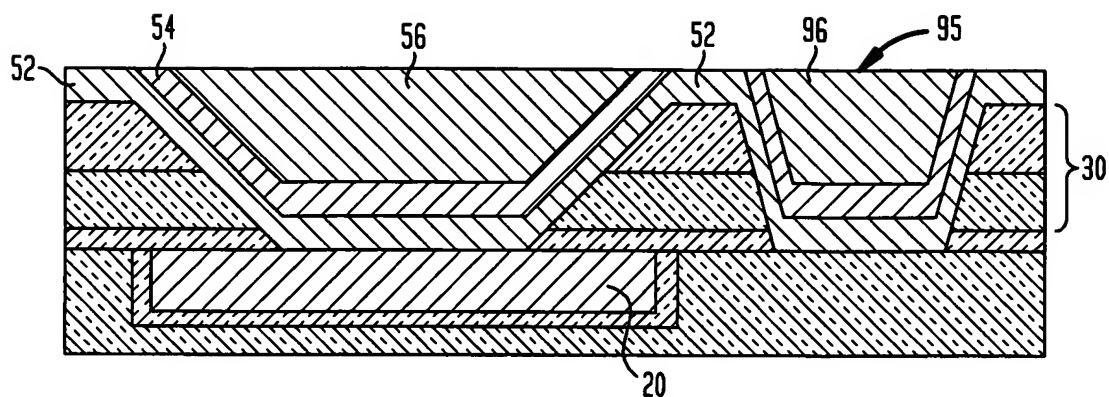
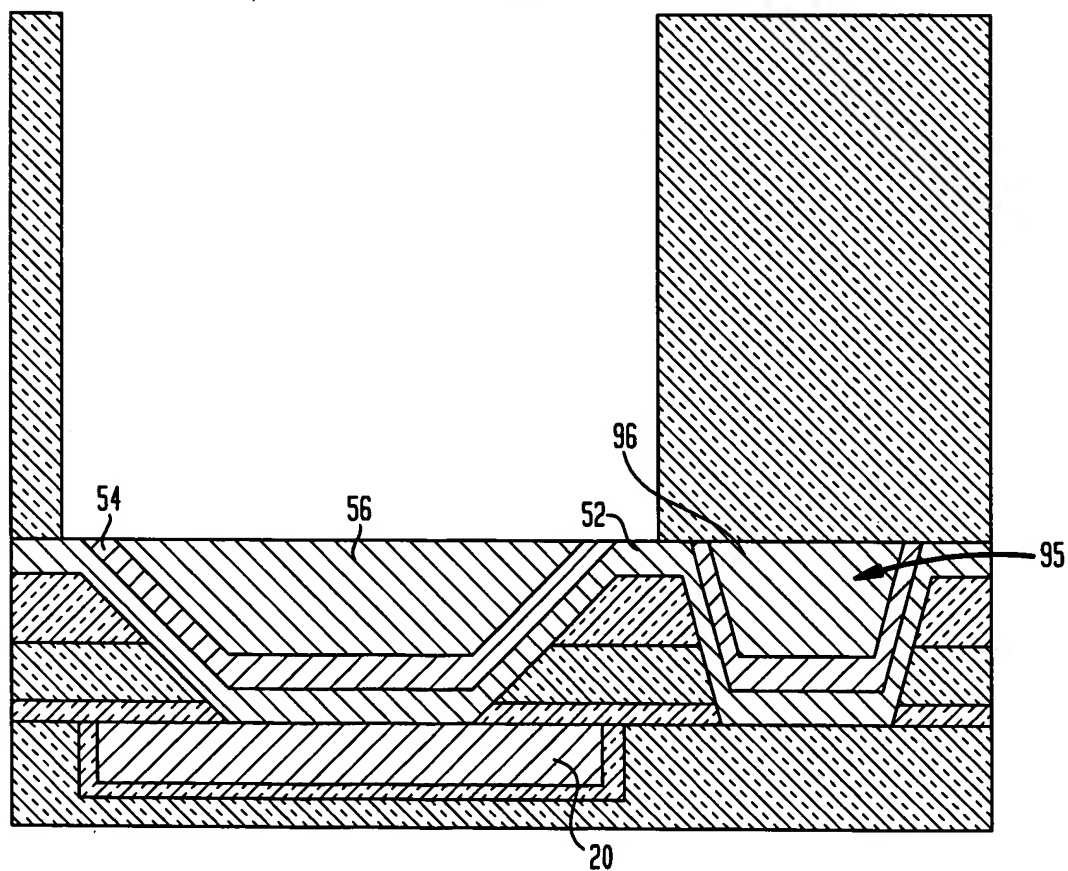


FIG. 3B



6/6

FIG. 3C

